



**HAL**  
open science

## Statically constrained nonlinear models with application to IC buffers

Chérif El Valid Diouf, Mihai Telescu, Noël Tanguy, Pascale Cloastre, Igor Simone Stievano, Flavio G. Canavero

► **To cite this version:**

Chérif El Valid Diouf, Mihai Telescu, Noël Tanguy, Pascale Cloastre, Igor Simone Stievano, et al.. Statically constrained nonlinear models with application to IC buffers. 15th IEEE Workshop on Signal Propagation on Interconnects (SPI), 2011, May 2011, Naples, Italy. pp.115-118, 10.1109/SPI.2011.5898853 . hal-00666877

**HAL Id: hal-00666877**

**<https://hal.univ-brest.fr/hal-00666877>**

Submitted on 6 Feb 2012

**HAL** is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

# Statically constrained nonlinear models with application to IC buffers

C. Diouf<sup>1</sup>, M. Telescu<sup>1</sup>, N. Tanguy<sup>1</sup>, P. Cloastre<sup>1</sup>, I.S. Stievano<sup>2</sup>, F.G. Canavero<sup>2</sup>

<sup>1</sup> Université Européenne de Bretagne, France. Université de Brest ; CNRS, UMR 3192 Lab-STICC, ISSTB

<sup>2</sup> Politecnico di Torino, Italy

Reference contact: [mihai.telescu@univ-brest.fr](mailto:mihai.telescu@univ-brest.fr)

## Abstract

Volterra series are well known approaches to modeling non-linear systems. In recent works a properly weighted combination of Volterra models have been used to successfully mimic the behavior of the output port of an integrated circuit buffer. The current paper focuses on a novel mechanism for controlling the static characteristic of such models. A commercial driver example is used to illustrate the efficiency of the technique to guarantee accurate static levels during time-domain simulations.

## Introduction

The modeling of digital Integrated Circuits (IC) input and output buffers is a vital stage in the assessment of signal integrity and electromagnetic compatibility in high-end digital systems. Buffers both drivers and receivers act as non-linear terminals of the interconnect line networks and their transient behavior may have unwanted effects on the proper functioning of the system. In recent years, researchers have concentrated their attention on the development of behavioral models that seek to mimic the device's input-output behavior of ten proves more efficient from a computational point of view than attempting to model its internal physics.

Different approaches exist ranging from neural networks [1] to more common two-piece models [2-3] exploiting the two-state nature of the device. In a previous paper [4] some of the authors showed that two properly weighted weakly non-linear Volterra models could successfully be used to mimic the output behavior of drivers. The strength of this approach resides in a greater degree of generality allowing for greater adaptability.

In this paper the authors focus on the static characteristic of Volterra models and the way in which they may be forced to fit a certain form. This is particularly interesting in the field of buffer modeling where the static behavior of the original system needs to be accurately reproduced.

## Volterra and Volterra-Laguerre models

Volterra series are a well-known input-output representation of nonlinear systems and have found in the last half-century a variety of applications in different fields of science. They have proved to be a valuable and reliable tool in nonlinear system identification and allow a straightforward generalization to multivariable systems. The general discrete time expression of a Volterra series is given by

$$\begin{aligned}
 y(k) &= \sum_{k_1=0}^{\infty} h_1(k_1)x(k-k_1) \\
 &+ \sum_{k_1=0}^{\infty} \sum_{k_2=0}^{\infty} h_2(k_1, k_2)x(k-k_1)x(k-k_2) \\
 &+ \dots \\
 &= \sum_{m=1}^{\infty} \sum_{k_1=0}^{\infty} \dots \sum_{k_m=0}^{\infty} h_m(k_1, k_2, \dots, k_m) \prod_{l=1}^m x(k-k_l)
 \end{aligned} \tag{1}$$

with  $k$  being the discrete time,  $y$  the output variable,  $x$  the input and  $h_m$  being referred to as the  $m$ -th order kernel. One may intuitively see Volterra series as a generalization of a linear systems impulse response function and it is quite obvious that for  $m = 1$  expression (1) is reduced to the classical impulse response function of a linear system.

In practice direct identification of the kernels, for an unknown nonlinear system, may prove cumbersome. A common approach is to use a projection on an orthogonal basis. The discrete Laguerre basis has been chosen to illustrate the approach presented in this paper, although the mathematical derivations may be extended to other representations.

The discrete Laguerre functions  $\phi_i(k)$  may be defined by their  $z$  transform as follows

$$\Phi_i(z) = \sqrt{1-a^2} \frac{z}{z-a} \left( \frac{1-az}{z-a} \right)^i, \quad i = 0, 1, \dots \tag{2}$$

Laguerre functions form a complete set of orthonormal functions in  $\ell^2(\mathfrak{X}_+)$ , then each Volterra kernel can be expressed with a multidimensional Laguerre series as

$$\begin{cases}
 h_1(k_1) = \sum_{i_1=0}^{\infty} C_{i_1} \phi_{i_1}(k_1) \\
 h_2(k_1, k_2) = \sum_{i_1=0}^{\infty} \sum_{i_2=0}^{\infty} C_{i_1, i_2} \phi_{i_1}(k_1) \phi_{i_2}(k_2) \\
 \dots
 \end{cases} \tag{3}$$

Let  $\bar{\phi}_i(k)$  denote the response of the  $i$ th Laguerre filter with transfer function given by (2) to the input  $x(k)$ , i.e.

$$\bar{\phi}_i(k) = \phi_i(k) * x(k) = \sum_{k_j=0}^{\infty} \phi_i(k_j) x(k-k_j). \tag{4}$$

It follows that equation (1) can be recast as

$$\begin{aligned}
y(k) &= \sum_{i_1=0}^{\infty} C_{i_1} \bar{\phi}_{i_1}(k) \\
&+ \sum_{i_1=0}^{\infty} \sum_{i_2=0}^{\infty} C_{i_1, i_2} \bar{\phi}_{i_1}(k) \bar{\phi}_{i_2}(k) \\
&+ \dots \\
&= \sum_{m=1}^{\infty} \sum_{i_1=0}^{\infty} \dots \sum_{i_m=0}^{\infty} C_{i_1, i_2, \dots, i_m} \prod_{l=1}^m \bar{\phi}_{i_l}(k)
\end{aligned} \tag{5}$$

Thus, with a known well chosen input signal and the respective, known, output signal one may compute the expansion coefficients  $C_{i_1}, C_{i_1, i_2}, \dots, C_{i_1, i_2, \dots, i_m}$ . Subsequently the response to any input signal may be obtained.

In practice a finite number of kernels is considered  $m < M$ , and the expansion of each kernel is truncated at an order  $N_m$ . Due to the truncation error the choice of parameter  $a$  in (2) is non trivial. Further more, different parameters can be used for the different kernels. These aspects are discussed in [5] and [6].

### The static characteristic of a Volterra-Laguerre model

It may be shown that the static characteristic of a Volterra-Laguerre model is a polynomial given by the  $z$ -transform of  $y(k)$  for  $z = \exp(j\omega)$  at  $\omega = 0$ , i.e

$$Y(e^{j0}) = p_1 A + p_2 A^2 + \dots + p_M A^M \tag{6}$$

where  $A$  denotes the amplitude of the input and

$$p_m = \left( \sqrt{\frac{1+a}{1-a}} \right)^m \sum_{i_1=0}^{N_m-1} \sum_{i_2=0}^{N_m-1} \dots \sum_{i_m=0}^{N_m-1} C_{i_1, i_2, \dots, i_m} \tag{7}$$

for  $m = 1, 2, \dots, M$ .

Let us now suppose that we wish to optimally modify the coefficients of the expansion with the idea to force a new static characteristic given by

$$Y'(e^{j0}) = \sigma_1 A + \sigma_2 A^2 + \dots + \sigma_M A^M \tag{8}$$

Correcting each coefficient of the polynomial is a classical optimization problem under linear constraints of the type  $L_m \vec{C}_m - \sigma_m$  that can be solved using Lagrange multipliers. In [7] it has been shown that in the particular cases of orthogonal projections the solution is given by

$$\vec{C}'_m = \vec{C}_m - L_m^T (L_m L_m^T)^{-1} (L_m \vec{C}_m - \sigma_m), \quad m = 1, 2, \dots, M \tag{9}$$

where vectors  $\vec{C}_m$  and  $\vec{C}'_m$  respectively stack all the uncorrected and corrected coefficients of the  $m^{\text{th}}$  order kernel

$$\vec{C}_m = \left[ C_{0, \dots, 0, 0}, C_{0, \dots, 0, 1}, \dots, C_{N_m-1, \dots, N_m-1, N_m-1} \right]^T,$$

and line vector  $L_m$  contains the constraint

$$L_m = \left( \sqrt{\frac{1+a}{1-a}} \right)^m [1, 1, \dots, 1].$$

After simplification the solution is given by the following correction of individual coefficients

$$C'_{i_1, i_2, \dots, i_m} = C_{i_1, i_2, \dots, i_m} - \left( \sqrt{\frac{1-a}{1+a}} \right)^m \frac{1}{N_m^m} (p_m - \sigma_m). \tag{10}$$

### Two-state Volterra models for IC buffers

The method presented in [2] seeks to model the output port of a buffer (fig. 1) by expressing the output current  $i(t)$  as a weighted sum of  $i_H(t)$  and  $i_L(t)$  respectively corresponding to the high-state and the low-state output currents. Each term non-linearly depends on the output voltage  $v(t)$ . The expression of the output current is then given by

$$i(t) = w_H(t) i_H(v(t), t) + w_L(t) i_L(v(t), t). \tag{11}$$

This assumption is justified by the inclusion in the model equations of the inherent logical switching operation of this class of devices. This solution has been demonstrated to provide good results in terms of both model accuracy and model efficiency.

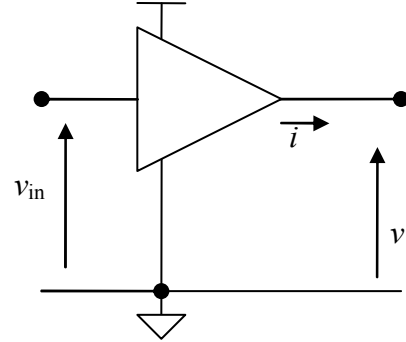


Fig. 1. Single ended output buffer.

As presented in [4], each sub-system, non-linearly binding  $i_H(t)$  to  $v(t)$  and

$i_L(t)$  to  $v(t)$  may then be approximated by a Volterra-Laguerre model.

At this stage the new technique described in the previous section may be used to guarantee a good static characteristic of the buffer model. Note that in practice buffer static characteristics are not necessarily polynomial. Furthermore, even if one accounts for worst case scenarios only a part of the characteristic will be explored in practice. Therefore the best algorithm to be used is

- 1) For each sub-model (high state and low state) identify the region of interest of the static characteristic.
- 2) Fit the characteristic with a polynomial according to (8).
- 3) Use (10) to correct the coefficients of the Volterra-Laguerre expansions to fit the desired characteristic.

In the case of differential drivers (fig. 2), one has to account for the two output ports. Thus, eq. (11) needs to be generalized as follows

$$i_\lambda(t) = w_{\lambda, H}(t) i_{\lambda, H}(v_c(t), v_d(t), t) + w_{\lambda, L}(t) i_{\lambda, L}(v_c(t), v_d(t), t)$$

where  $\lambda = 1, 2$ ,  $v_c = (v_1 + v_2)/2$  and  $v_d = (v_1 - v_2)$  are respectively the common and differential mode voltages.

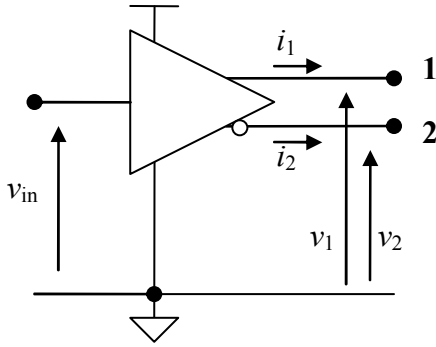


Fig.2. Differential driver.

The generalization of the Volterra-Laguerre expansion given by (5) is straightforward but leads to a lengthy expression which is why it was presented in the appendix. Obviously the static characteristic will in this case be bi-dimensional and its correction will require a bi-dimensional fit. This generalization does not pose any particular problems, it relates to basic spatial geometry. The last step of the algorithm remains essentially unchanged; the correction of individual coefficients is performed in the same way.

### Simulation results

In most cases the identification technique used in [4] works well on most drivers and the “blindly” identified static characteristic is reproduced with sufficient accuracy. Nonetheless, as shown in the following example, in some cases, particularly when a very compact model is computed, the static characteristic of the model may diverge from that of the original system. In the event of such scenarios, the procedure described in this paper becomes a useful and elegant tool. The user is no longer forced to increase the model complexity (i.e. the truncation order of the kernels) but may simply resort to a statically constrained solution.

A commercial differential driver was used for the tests presented below. Its two output ports were modeled using a two-kernel Volterra-Laguerre model with a total of four Laguerre functions for the 1st order kernel and four for the 2nd order. Figures 3a) and 3b) show the common mode voltage  $v_c$  sweep of the on-state static characteristic of the driver for different values of the differential voltage  $v_d$ .

Figure 4 shows the result of a dynamic simulation tracing the differential mode response of both the original system and the model for a “010” sequence. It should be noted that the correction of the static characteristic is effective well outside the working range of the device as shown in figure 3 and that it is achieved without visible damage to the dynamic performance as seen in figure 4. Indeed, the overshoots and undershoots are accurately reproduced.

### Conclusion

In the behavioral modeling of digital IC buffers one needs to show particular care in accurately reproducing the static characteristics of the devices. This short paper addresses the problem of computing buffer models using Volterra-Laguerre series while guaranteeing a correct static characteristic. The

key feature is an *a posteriori* optimal correction technique which enables the user to analytically force a specific static characteristic on the model without damaging its dynamic qualities.

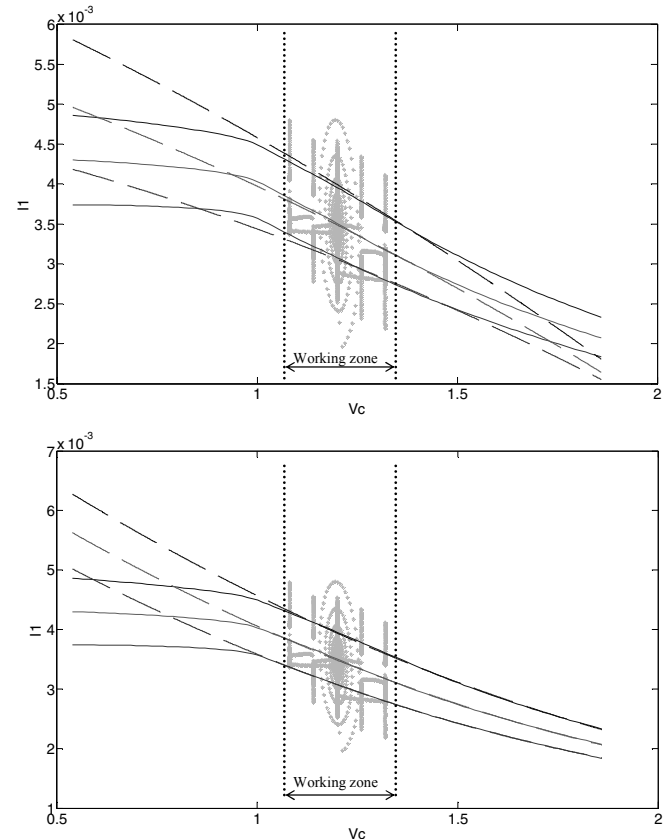


Fig. 3a and 3b. Solid lines: static characteristics of a commercial driver: output current  $i_1$  versus common mode voltage  $v_c$  for different values of the differential voltage  $v_d$ . Dashed lines: characteristics of the uncorrected (3a) and corrected (3b) models. Grey stars: values used in the identification signals.

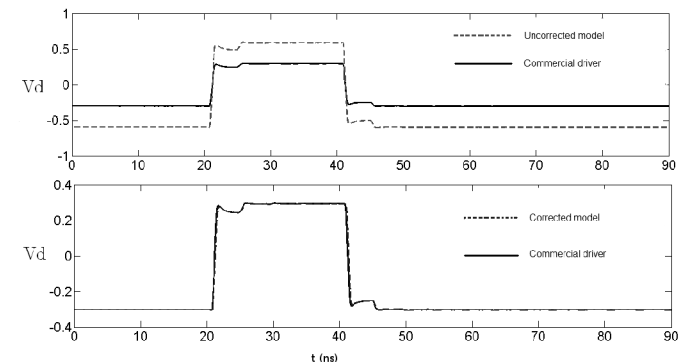


Fig. 4. Differential-voltage transient response of uncorrected (4a) and corrected (4b) models versus the original.

### Appendix

The expression of a two input Volterra-Laguerre series as used in the identification of differential drivers is

$$\begin{aligned}
 y(k) = & \sum_{i_1=0}^{\infty} C_{i_1}^{(1)} \bar{\phi}_{i_1}^{(1)}(k) \\
 & + \sum_{i_1=0}^{\infty} C_{i_1}^{(2)} \bar{\phi}_{i_1}^{(2)}(k) \\
 & + \sum_{i_1=0}^{\infty} \sum_{i_2=0}^{\infty} C_{i_1, i_2}^{(1,1)} \bar{\phi}_{i_1}^{(1)}(k) \bar{\phi}_{i_2}^{(1)}(k) \\
 & + \sum_{i_1=0}^{\infty} \sum_{i_2=0}^{\infty} C_{i_1, i_2}^{(1,2)} \bar{\phi}_{i_1}^{(1)}(k) \bar{\phi}_{i_2}^{(2)}(k) \\
 & + \sum_{i_1=0}^{\infty} \sum_{i_2=0}^{\infty} C_{i_1, i_2}^{(2,1)} \bar{\phi}_{i_1}^{(2)}(k) \bar{\phi}_{i_2}^{(1)}(k) \\
 & + \sum_{i_1=0}^{\infty} \sum_{i_2=0}^{\infty} C_{i_1, i_2}^{(2,2)} \bar{\phi}_{i_1}^{(2)}(k) \bar{\phi}_{i_2}^{(2)}(k) \\
 & + \dots
 \end{aligned}$$

where  $\bar{\phi}_i^{(1)}(k) = \phi_i(k) * x^{(1)}(k)$  and  $\bar{\phi}_i^{(2)}(k) = \phi_i(k) * x^{(2)}(k)$ .

### Acknowledgements

The authors would like to acknowledge the support provided by the *Institut Supérieur des Sciences et Technologie de Brest (ISSTB)*.

### References

- [1] Yi Cao, Runtao Ding, Qi-Jun Zhang, "State-Space Dynamic Neural Network Technique for High-Speed IC Applications: Modeling and Stability Analysis," IEEE Transactions on Microwave Theory and Techniques, Vol. 54, No. 6, pp. 2398–2409, June 2006.
- [3] I. S. Stievano, I. A. Maio, F. G. Canavero, "M $\pi$ log Macromodeling via Parametric Identification of Logic Gates," IEEE Transactions on Advanced Packaging, Vol. 27, No. 1, pp. 15–23, Feb. 2004.
- [3] B. Mutnury, M. Swaminathan, J. P. Libous, "Macromodeling of nonlinear digital I/O drivers," IEEE Transactions on Advanced Packaging, Vol. 29, No. 1, pp. 102–113, Feb. 2006.
- [4] M.G. Telescu, I.S. Stievano, F.G. Canavero, N. Tanguy, "An Application of Volterra Series to IC Buffer Models", 14th IEEE Workshop on Signal Propagation On Interconnects, Hildesheim, Germany, May 9-12, 2010
- [5] R. J.G.B. Campello, G. Favier, "Optimal Expansions of Discrete-Time Volterra Models Using Laguerre Functions," Automatica, Vol. 40, No. 5, pp. 815–822, May 2004.
- [6] A. Y. Kibangu, G. Favier, M. M. Hassani, "Laguerre-Volterra Filters Optimization Based on Laguerre Spectra," Journal on Applied Signal Processing, Vol. 2005, No. 17, pp. 2874-2887, 2005.
- [7] L.C. Calvez, P. Vilbé, P. Glouannec, "Orthonormal and nonorthonormal least squares approximation of a function subject to linear equality constraints" IEEE Trans. on