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Analog Performance Prediction Based on Archimedean Copulas Generation Algorithm

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Abstract—Testing analog circuits is a complex and very time consuming task. In contrary to digital circuits, testing analog circuits needs different configurations, each of them targets a certain set of output parameters which are the performances and the test measures. One of the solutions to simplify the test task and optimize test time is the reduction of the number of to-be-tested performances by eliminating redundant ones. However, the main problem with such a solution is the identification of redundant performances. Traditional methods based on calculation of the correlation between different performances or on the defect level are shown to be not sufficient. This paper presents a new method based on the Archimedean copula generation algorithm. It predicts the performance value from each output parameter value based on the dependence (copula) between the two values. Therefore, different performances can be represented by a single output parameter; as a result, less test configurations are required. To validate the proposed approach, a CMOS imager with two performances and one test measure is used. The simulation results show that the two performances can be replaced by a single test measure. Industrial results are also reported to prove the superiority of the proposed approach.¹

I. INTRODUCTION

In the functional test of analog circuits, the performances of each circuit are measured and verified to check if they are inside the specifications or not. If all the specifications are verified then the circuit is considered as functional (good) otherwise, it is considered as faulty, and thus it is rejected. However, if the number of the specifications is important, test time becomes more important and has a direct consequence on the test cost. For some analog circuit with a very high number of performances, the functional test is practically impossible to perform. Reducing this number is mandatory. The major problem in reducing the set of the performances is that it generates an important number of faulty circuits which will be sold and hereby increase the value of the defect level (proportion of faulty circuits that pass the test). The test engineers have to be careful in choosing a subset for which the defect level remains constant or increase only slightly. Until now, there is no efficient method that can reduce the set of the performances significantly. In this work we will try to eliminate a performance by predicting its values by observing another output parameter (test measure and performance). The idea is based on using the dependency structure (copula) of

the output parameters. A copula is a multivariate uniform distribution. For the case of a two dimensional copula, if a set of values of the first dimension is generated from the univariate uniform distribution than the values of the second dimension can be obtained by applying a simple transformation function. One of the advantages of this method is that it can consider a non linear dependency between the output parameters. Let us consider a performance that we want to predict from an output parameter for an initial set of produced circuits. Some of these circuits will be taken in order to calculate the copula between the considered output parameter and this performance. Then, this copula will be used to predict the performance of the other circuits of the initial set by calculating only the value of the output parameter. This approach will be detailed in this paper. In the next section, we will review some previous work. In section III, we will introduce the Archimedean copulas theory. In Section IV, we will present the proposed methodology used to predict any performance from any output parameter. Section V will present the application of this method for the case study of a CMOS imager. Finally, we conclude the paper with some future work.

II. PREVIOUS WORK

Several methods are proposed to reduce the time required in the functional test. Some methods are based on ordering the performances [1] [2] [3]. In these methods the first tested performances are those that have a high probability to detect faulty circuits. Decision binary trees have been used in [4], where binary classifiers are used to compact the complete test set by eliminating redundant tests. In [5] a subset of specifications is chosen based on the defect level test metric. Other methods are based on ATPG (Automatic Test Pattern Generation) algorithms used to predict the performances from the circuit responses [6]. In [7] a regression model calculated from a subset of tests is used to predict other tests outside of the considered subset. With the same idea of using regression models, [8] proposed a technique that predicts the values of the performances from a small set of test measures. The method proposed in [9] tries to predict performances based on models built on the correlation of the output parameters. In [10] a fast exploration of the functional test space is used to determine redundancy in functional tests. Machine learning has also been

¹This work is supported by the ISSTB (Institut Supérieur des Sciences et Technologies de Brest), France.

used recently in [11] to learn the intricate mappings between a subset of functional tests and direct pass/fail decisions.

The idea that the non linear dependency between the output parameters exist and can be of major importance in circuit behavior leads us to consider the copulas theory to track these dependencies. In [12] we used the copulas theory to generate a large sample of circuits from a small one, in order to estimate the test metrics and to fix optimally the test limits with high precision. In this work, we propose a new method where any performance can be predicted from any output parameter by estimating their copula. This copula can be estimated from an initial small subset of produced circuits. Then, a new copula with the same parameters will be generated using the values of the considered output parameter. This new copula will then be used to predict the performance values of all the circuits. The problem of fixing the test limits is not reported here because the limits of the predicted performance are the same as the one of the initial performance (i.e., the specification). Also, the test limits can only be fixed at the design stage from the Monte Carlo simulation. However, the proposed method can be applied directly to a real set of produced circuits.

III. ARCHIMEDEAN COPULAS THEORY

A. Estimating the copula

The theory of the Copulas will not be described formally in this paper. Basic definitions and properties of copula functions are presented in [13]. A succinct introduction to copulas for test metrics estimation is given in [12][14]. To illustrate how to calculate copulas from any sample, we propose to consider the bivariate example of Figure 1.

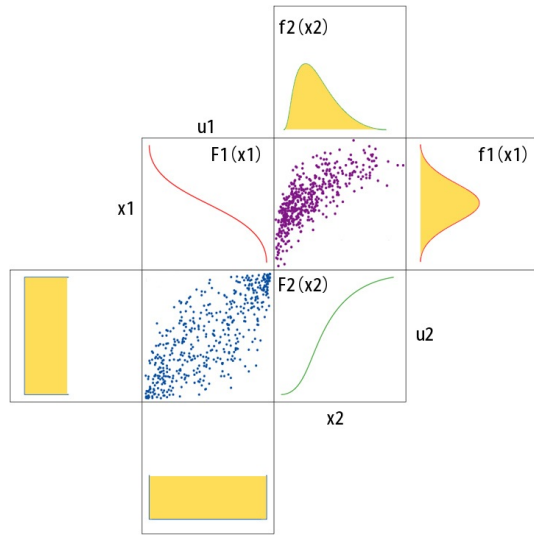


Fig. 1. Calculating the copula from a population.

The scatter plot of a bivariate random vector $X = (X_1, X_2)$ is shown in the upper right corner of Figure 1. To separate the dependencies between these two random variables from their marginal distributions, we apply the transformation $u_i = F_i(x_i)$, where each initial sample point (x_1, x_2) is transformed

into a new point (u_1, u_2) using the marginal CDF (Cumulative Density Function) of each variable (F_1 for x_1 and F_2 for x_2). The result of this transformation is the bivariate random vector $U = (U_1, U_2)$ shown in the lower left corner. This new sample distribution corresponds to an empirical copula for which the marginal distributions are uniform. This complete and scale-free description of dependence is more suitable to be fit to well known multivariate parametric laws called copula.

B. Archimedean copulas

Archimedean copulas include a large variety of copula families that can be easily constructed to model non linear dependencies and non elliptical distributions. For example, Archimedean copulas can describe asymmetric dependencies, where the dependence coefficients in the upper and the lower tails are different. Some basics about Archimedean copulas will be discussed in this section. The dependence coefficient Kendall's τ will be considered instead of the classical linear correlation factor ρ . An estimator $\hat{\tau}$ of this coefficient is calculated as follows:

$$\hat{\tau} = \frac{2}{n(n-1)} \sum_{i < j} \text{sgn}[(x_i - x_j)(y_i - y_j)], \quad i, j = 1, \dots, n \quad (1)$$

where

$$\text{sgn}(z) = \begin{cases} 1 & \text{if } z \geq 0 \\ -1 & \text{if } z < 0 \end{cases}$$

and, $(x_1, y_1), \dots, (x_n, y_n)$ are n observations from a vector (X, Y) of continuous random variables.

A two dimensional Archimedean copula is a function C from $[0, 1]^2$ to $[0, 1]$ which has the following form:

$$C(u_1, u_2) = \begin{cases} \varphi^{-1}(\varphi(u_1) + \varphi(u_2)) & \text{if } \varphi(u_1) + \varphi(u_2) \leq \varphi(0) \\ 0 & \text{otherwise} \end{cases} \quad (2)$$

where φ is called the generator function of the Archimedean copula. Notice that the generator function allows to write the copula as a sum of functions of the marginal distributions. For $0 \leq u \leq 1$, φ is defined as $\varphi(1) = 0$, $\varphi'(u) < 0$ and, $\varphi''(u) > 0$.

In the following, we will present two families of Archimedean copulas. The first one is the Gumbel copula family and the second one is the Clayton copula family. These copulas are used in the test vehicle in order to predict performance values from those of the output parameters.

1) Gumbel Copula:

The Gumbel copula [13] is an Archimedean copula whose generator function is defined as:

$$\varphi(u) = (-\ln(u))^\theta \quad (3)$$

with, $\theta \in [1, +\infty[$. For the two dimensional case its associated function $C(u_1, u_2)$ can be written as follows:

$$C(u_1, u_2) = \exp\left(-\left[(-\ln(u_1))^\theta + (-\ln(u_2))^\theta\right]^{1/\theta}\right) \quad (4)$$

The parameter θ depends on Kendall's τ and is calculated as follows:

$$\theta = \frac{1}{1 - \tau} \quad (5)$$

Figure 3(a) shows the CDF of the Gumbel copula with a parameter $\theta = 1.26$ and Figure 2(b) shows a set of 1000 samples generated from this copula.

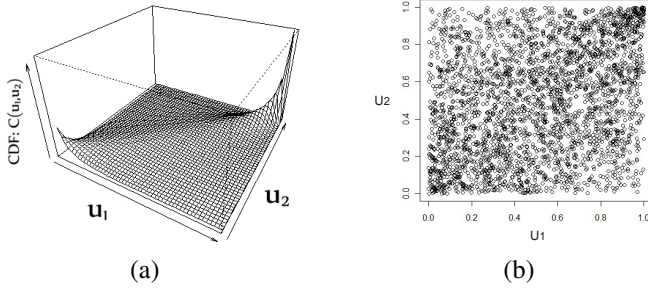


Fig. 2. (a) CDF of a Gumbel copula with $\theta = 1.26$, (b) 1000 samples generated from the Gumbel copula.

2) Clayton Copula:

The Clayton copula [16] [17] is an Archimedean copula whose generator function is defined as:

$$\varphi(u) = \frac{1}{\theta}(u^{-\theta} - 1) \quad (6)$$

with, $\theta \in] -1, 0[\cup] 0, \infty[$. For the two dimensional case its associated function $C(u_1, u_2)$ can be written as follows:

$$C(u_1, u_2) = \max \left([u_1^{-\theta} + u_2^{-\theta} - 1]^{-1/\theta}, 0 \right) \quad (7)$$

The generalized form of this equation is given in [13]. The parameter θ depends on Kendall's τ and is calculated as follows:

$$\theta = -\frac{2\tau}{\tau - 1} \quad (8)$$

Figure 3(a) shows the CDF of the Clayton copula with a parameter $\theta = -0.63$ and Figure 3(b) shows a set of 1000 samples generated from this copula.

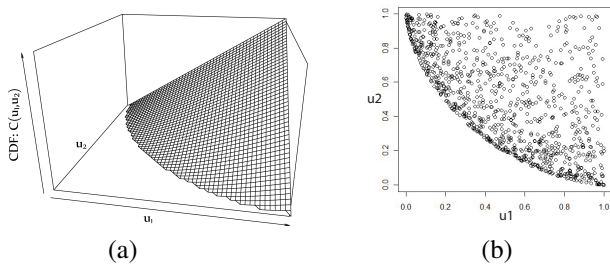


Fig. 3. (a) CDF of a Clayton copula with $\theta = -0.63$, (b) 1000 samples generated from the Clayton copula.

3) *Archimedean copulas generation algorithm:* To generate n samples from a 2-dimensional Archimedean copula with the generator φ we use the following Marshall & Olkin algorithm [15] which is illustrated by Figure 4:

- (1) Generate n independent variables z_1, \dots, z_n with distribution function D ($Z \sim$ Gamma distribution for the Clayton copula and $Z \sim$ Positive Stable distribution for the Gumbel copula, cf. Table I).
- (2) Generate n independent variables x_1, \dots, x_n with the uniform distribution $X \sim \mathcal{U}[0, 1]$.
- (3) Generate n independent variables y_1, \dots, y_n with the uniform distribution $Y \sim \mathcal{U}[0, 1]$.
- (4) Return $((u_1, v_1), \dots, (u_n, v_n))$, where, $u_i = \varphi(-\ln(x_i)/z_i)$ and $v_i = \varphi(-\ln(y_i)/z_i)$, $i \in 1, \dots, n$.

The parameters of the distribution D used in this algorithm are given in the following Table I.

Copula family	Distribution D	Parameters of D
Clayton	Gamma (Γ)	$\Gamma(1/\theta, 1)$
Gumbel	Positive Stable (St)	$St(1/\theta, 1, (\cos(\pi/(2\theta)))^\theta, 0)$

TABLE I
PARAMETERS OF THE D DISTRIBUTION.

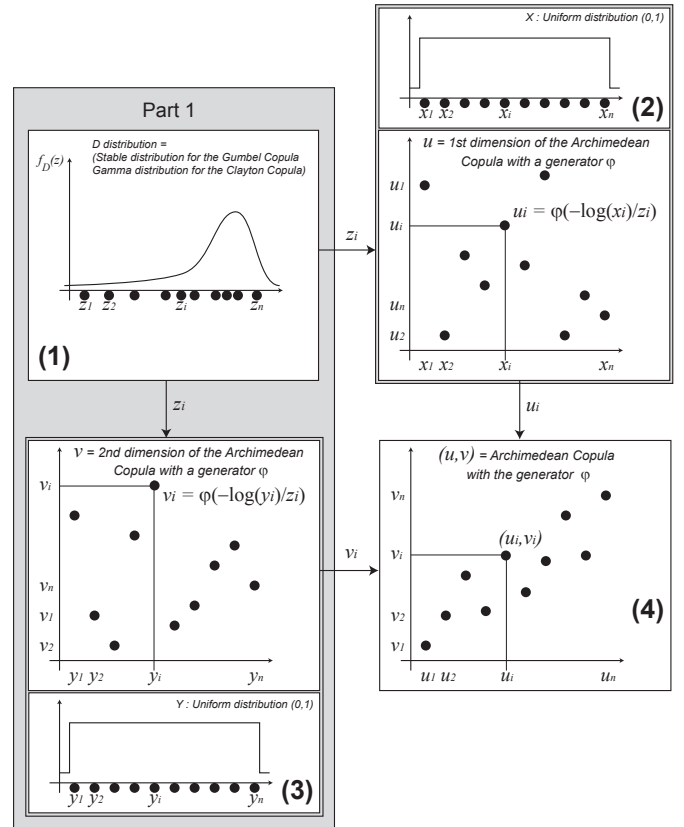


Fig. 4. Illustration of the Marshall & Olkin algorithm.

IV. PERFORMANCE PREDICTION METHOD

The algorithm presented above is the main part of the proposed method used to predict performance values from values of any output parameter. Figure 5 shows the steps of the proposed method. Let S be a set of n produced circuits, $P1$ a considered performance and T the output parameter which will be used to predict the values of $P1$. We will consider a subset of only $m \ll n$ circuits. From this subset, we estimate the copula $C1$ of $P1$ and T , i.e., $C1 = (V, W1)$, where $V = CDF(T)$ and $W1 = CDF(P1)$. Note that, V and $W1$ have a uniform distribution $\mathcal{U}[0, 1]$. The algorithm of Marshall & Olkin presented above can be used to generate another copula $C2(V, W2)$ with the same parameters as those of $C1(V, W1)$. We can consider this procedure as a method that predicts the values of $W1$ knowing the values of V . Therefore, using $C2$ we can then predict the values of $W1$ of all the circuits. Finally, we can calculate the values of the performance $P1$ of all the circuits by calculating the inverse CDF of $W2$, i.e., $P1 \approx P2 = CDF^{-1}(W2)$.

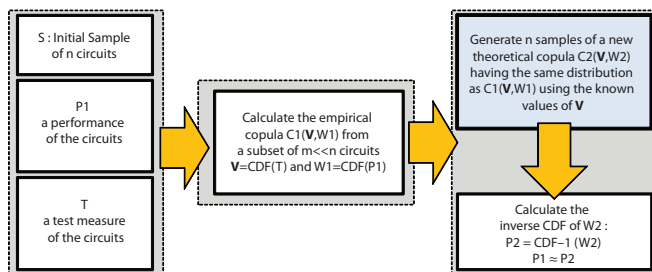


Fig. 5. Performance prediction method.

V. TEST VEHICLE

The considered case-study is a BIST technique for a CMOS imager presented in [18]. The analog and mixed-signal parts of the imager include a large pixel matrix, the column amplifiers and the data converters. The pixel matrix usually composed of millions of pixels is typically read line by line through the column amplifiers. Figure 6 shows the pixel structure composed by PMOS transistors and a photodiode. This type of pixel gives a logarithmic relationship between the output voltage (V_{ph}) and the incoming light (represented as the photogenerated current I_{ph}).

The main performance measured for the pixel matrix and the column amplifier are the Fixed Pattern Noise (FPN). The FPN represents the difference that exists between two pixels (or two column amplifiers) under constant illumination. Different light sources are used to measure this noise. Two major kinds of FPN are measured: Pixel Response Non Uniformity (PRNU) that is obtained by using light sources and Dark Signal Non Uniformity (DSNU) that is obtained under dark conditions.

The BIST technique consists of the application of a voltage pulse at the anode of the photodiode, to measure the output voltage VA of the pixel. The whole analog ground of the pixel is externally pulsed (not shown in Figure 6). This electrical measurement is performed very fast, and the output

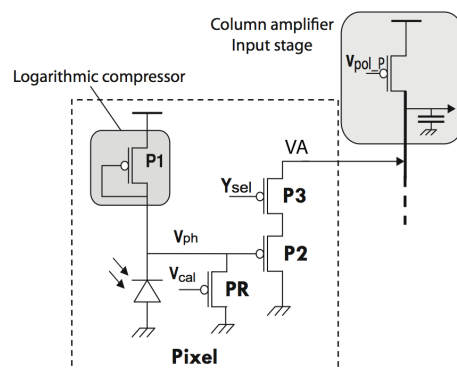


Fig. 6. Logarithmic pixel structure.

measurement is thus not dependent on the incoming light. This BIST measurement is intended to capture the major sources of DSNU, such as transistor mismatches.

The specifications of the two performances are $PRNU \in [-0.0306, 0.0306]$ Volt, and $DSNU \in [-0.0216, 0.0216]$ Volt are fixed at 2σ which leads to a yield of ≈ 460 dppm (defect part per million, i.e. 460 pixels out of specifications in 1 million). Since we have only 16383 pixels, if the specifications are fixed to 3σ or more, only a few faulty circuits (≈ 0) will be generated. That is why we choose to fix the specifications at 2σ to generate an important number of defect circuits. Then, the proposed method can be evaluated efficiently by comparing the original yield calculated from the initial performances and the predicted yield obtained from the predicted performances.

A. Performance prediction of the CMOS imager

In our case study, we will try to predict the values of the PRNU and the DSNU of each circuit just by calculating the values of VA . Thus, we will take a subset of $m = 3000$ circuits from which the empirical copula of VA and PRNU (or DSNU) will be estimated. This one will then be used to generate another theoretical copula with the same parameters by taking into account the values of VA . Finally, this new copula will be used to predict the performance values from the values of the VA . For more clarity, we detail this method in the following steps :

- 1) Let S be the set of 16383 pixels
- 2) Let $T = VA$ be the considered test measure
- 3) Let $P1 = PRNU$ (or $P1 = DSNU$) be the performance that we want to predict from VA
- 4) Consider a small subset $S' \subset S$ of 3000 circuits
- 5) Estimate the empirical copula $C1(V, W1)$ in this subset (S'), where V is the CDF of T ($V = CDF(T)$) and $W1$ is the CDF of $P1$ ($W1 = CDF(P1)$).
- 6) Generate a set of 16383 values from a new theoretical copula $C2(V, W2)$ with the same parameters as $C1$ using the Marshall & Olkin algorithm presented in Section III-B.3. Note that the first dimension of this copula is the 16383 values of V , i.e., the CDF of T calculated from the set S .

7) Calculate $P2 = CDF^{-1}(W2)$. The values of $P2$ are close to the values of $P1$ predicted from V .

B. Results

We will use the steps presented above to predict the values of the first performance PRNU from those of the test measure VA. The same steps will be used to predict the values of the second performance DSNU from the test measure VA.

First, we will estimate the empirical copula of PRNU and VA by calculating $W1$, the CDF of PRNU and V , the CDF of VA from a subset of 3000 pixels. Figure 7 shows the obtained copula. If we compare this copula to the one of Figure 2(b) we conclude that it is a Gumbel copula. This comparison has been validated using a goodness of fit test of copulas, not discussed here for sake of simplicity. This copula is characterized by the parameter θ which is calculated from Kendall's τ correlation factor using the Equation (5). In our case, θ is equal to 1.26.

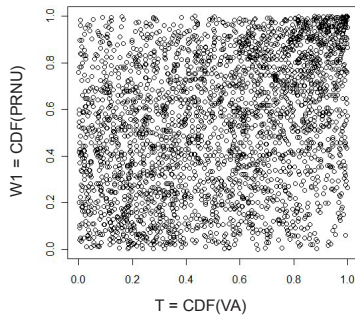


Fig. 7. 3000 samples of the empirical copula of the PRNU and VA.

Now, instead of considering only the 3000 circuits, we will take into consideration all the 16383 pixels. We will generate 16383 samples from the new theoretical Gumbel copula $C2(V, W2)$ with the parameter $\theta = 1.26$ in which the first dimension is equal to the values of the CDF of VA. Figure 8 shows the obtained copula.

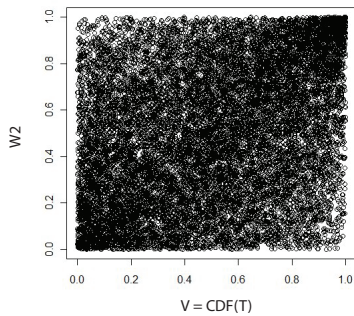


Fig. 8. 16000 samples generated from the theoretical Gumbel copula.

Then, by calculating the inverse CDF of $W2$ we will obtain the values of $P2$, the predicted performance, which is close to the PRNU. Figure 9 shows the joint distribution of the PRNU and the test measure VA obtained from the initial performance (in black o) and from the predicted performance (in gray +). It is clear that the two distributions are very close.

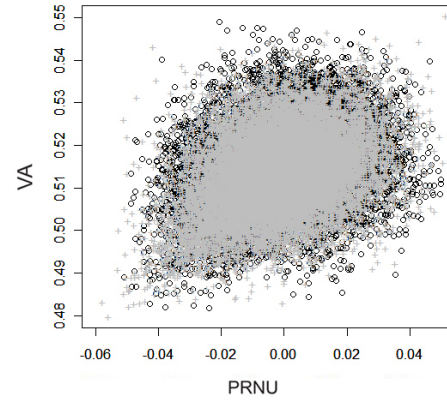


Fig. 9. The joint distribution of the test measure VA and the initial performance (black o) compared to the joint distribution of the test measure VA and the predicted performance (gray +).

Figure 10 shows the first 200 values of the initial performance (in black *) and those of the predicted performances for the same circuits (in red +). As we can see, the values are very close to each other. We have also compared the Yield (proportion of the good circuits) obtained in each case. It is equal to $Y1 = 462dppm$ in the initial PRNU and $Y2 = 474dppm$ in the predicted PRNU.

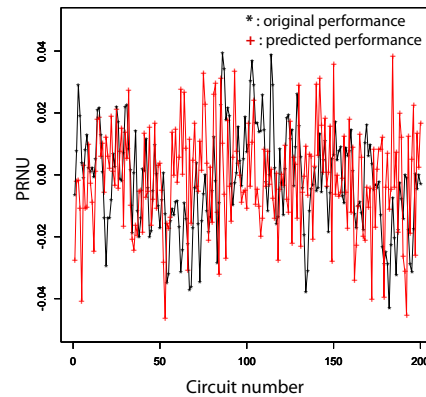


Fig. 10. A set of 200 values of the initial performances (black *) vs. those of the predicted performances (red +).

For the case of the second performance DSNU, we used the same procedure as presented above. Figure 11 shows the obtained copula between the DSNU and the test measure VA. If we compare it to the one of Figure 3(b) we conclude that it is a Clayton copula. A goodness of fit test of copula is used to validate this conclusion.

Following the procedure presented above for the case of the Gumbel copula, Figure 12 shows the values of the initial DSNU and those of the predicted DSNU. In this figure, only the first 200 values are drawn. It is clear that the two graphs are close. The Yield estimated in each case is equal to $Y1 = 466dppm$ in the initial DSNU and $Y2 = 454dppm$ in the

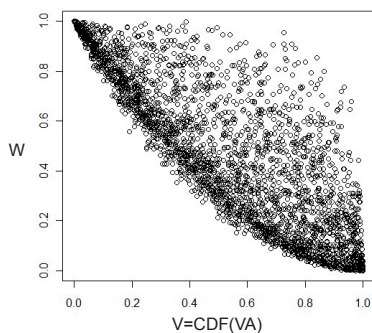


Fig. 11. 3000 samples of the empirical copula of the DSNU and VA.

predicted DSNU.

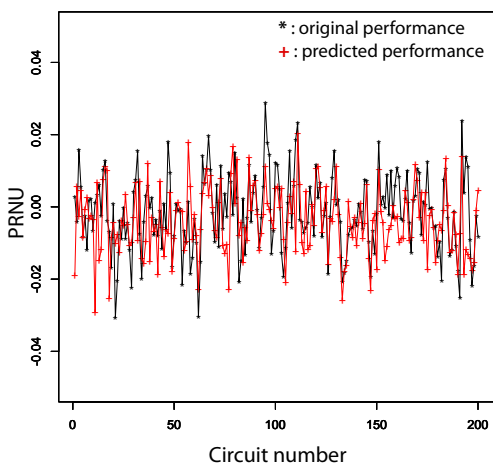


Fig. 12. A set of 200 values of the initial performances (black *) vs. those of the predicted performances (red +).

VI. CONCLUSIONS

This paper has presented a new method for the prediction of performance values from those of the output parameters (test measures and performances) in a set of produced circuits. The proposed method is based on the Archimedean copula generation algorithm. Marshall & Olkin's algorithm is used to generate from a uniform distribution another uniform distribution to obtain a dependent couple that follows any targeted copula. This algorithm has also been used to generate from the CDF of any output parameter, which is uniform, the CDF of any performance by estimating their copula. The method has been applied to a set of pixels of a CMOS imager with two performances PRNU and DSNU and one test measure VA. The obtained results based on the estimation of the Yield shows that the predicted performance is very close to the original one. Future work will target the use of the same method to fix the test limits without calculating the test metrics.

Copule	tau de dépendance		
α_1			
α_1			
α_1			

VII. ACKNOWLEDGEMENTS

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REFERENCES

- [1] L. Milor and A. L. Sangiovanni-Vincentelli, Minimizing production time to detect faults in analog circuits, *IEEE Transactions Computer-Aided Design of Integrated Circuits and Systems*, vol. 13, no. 6, pp. 796-813, 1994.
- [2] Nouredine Akkouche, Salvador Mir and Emmanuel Simeu, Ordering of Functional Tests Based on Parametric Defect Level Estimation, *28th IEEE VLSI Test Symposium (VTS'10)*, Santa Cruz, California, USA, April 2010.
- [3] S. D. Huss and R. S. Gyurcsik. Optimal ordering of analog integrated circuit tests to minimize test time, *28th ACM/IEEE Design Automation Conference*, 1991, pp. 494-499.
- [4] S. Biswas and R. D. Blanton. Statistical test compaction using binary decision trees, *IEEE Design and Test of Computers*, vol. 23, no. 6, 2006, pp. 452-462.
- [5] N. Akkouche, A. Bounceur, S. Mir, and E. Simeu, Minimization of functional tests by statistical modelling of analogue circuits, *Proceedings of International Conference on Design and Technology of Integrated Systems in nanoscale era (DTIS)*, 2007, pp. 35-40.
- [6] R. Voorakaranam and A. Chatterjee, Test Generation for Accurate Prediction of Analog Specifications, *Proceeding of VTS*, 2000, pp.137-142.
- [7] J. B. Brockman and S.W. Director Predictive subset testing: optimizing IC parametric performance testing for quality, cost, and yield, *IEEE Transactions on Semiconductor Manufacturing*, vol. 2, no. 3, pp. 104-113, 1989.
- [8] S. Battacharya, A. Halder, G. Srinivasan, and A. Chatterjee. *Alternate testing of RF transceivers using optimized test stimulus for accurate prediction of system specifications*. In Journal of Electronic Testing : Theory and Applications (JETTA), vol. 21, 2005, 323-339.
- [9] Jungran Lee, Walker D.M.H., Milor L., Yeng Peng, Hill G. IC performance prediction for test cost reduction, *IEEE International Symposium on Semiconductor Manufacturing Conference Proceedings*, Santa Clara, CA , USA, 1999, 111-114.
- [10] Gomes A. V. and A. Chatterjee. Distance constrained dimensionality reduction for parametric fault test generator, *Proceeding of Asia Test Symposium*, 2001, pp. 411-416.
- [11] H. G. Stratigopoulos, P. Drineas, M. Slamani, and Y. Makris, Non-RF to RF Test Correlation Using Learning Machines: A Case Study, *25th Proceedings IEEE VLSI Test Symposium*, 2007, pp. 9-14.
- [12] A. Bounceur, S. Mir and H-G. Stratigopoulos. Estimation of analog parametric test metrics using copulas. *IEEE Transactions Computer-Aided Design of Integrated Circuits and Systems*, September 2011, volume 30, number 09, ITCSDI, ISSN 0278-0070.
- [13] R.-B. Nelsen. *An Introduction to Copulas*. Lecture Notes in Statistics. Springer, New York, 1999.
- [14] K.Beznia A. Bounceur and S. Mir. R.Euler, Parametric test metrics estimation using non-Gaussian copulas, *In IEEE 17th International Mixed-Signals, Sensors and Systems Test Workshop (IMS3TW)*, Santa Barbara, California, USA, 2011, May 16-18.
- [15] A. W Marshall and I. Olkin. *Families of multivariate distributions*, Journal of the American Statistical Association, 1988, 83-403.
- [16] Clayton, D. G. *A model for association in bivariate life tables and its application in epidemiological studies of familial tendency in chronic disease incidence*. *Biometrika*, 1978, 65: 141-151.
- [17] Genest, C. and MacKay, R. J. (1986). *Copules archimédiennes et familles de lois bidimensionnelles dont les marges sont données*. *Canadian Journal of Statistics*, 14: 145-159.
- [18] Livier Lizarraga, Salvador Mir, Gilles Sicard, Experimental Validation of a BIST Technique for CMOS Active Pixel Sensors, *In the 27th IEEE VLSI Test Symposium VTS*, 2009, pp.189-194.