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Broadband Balun Using Active Negative Group Delay Circuit

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Abstract—In this paper, a broadband balun using an active circuit with negative group delay (NGD) is proposed. The unit cell of the active NGD circuit is based on a Field Effect Transistor (FET) in cascade with an RLC series network. First, a comparison between measurements of a two-stage prototype of this active topology and simulations validate the synthesis method of this innovative device. Then, thanks to the NGD circuit, a constant phase can be generated if this circuit is associated with a classical transmission line. By implementing such phase shifters into the two branches of a resistive splitter, we obtain a new balun topology. The NGD balun simulation results show a rather constant differential output phase ($180^\circ \pm 9^\circ$), insertion losses above -2.4 dB and an excellent isolation below -59 dB for all three ports, for a bandwidth from 3.5 GHz to 5.5 GHz.

I. INTRODUCTION

In the late 1960s, Veselago [1] has proposed the theory of materials exhibiting both negative permittivity and permeability. More than thirty years later, Pendry and Smith [2]-[3] have experimentally validated this theory for 3-D artificial media. Several experimentations [4]-[5] have since proposed and validated the implementation of such materials, also known as metamaterial, in 2-D and 1-D in planar technologies.

Under certain conditions, these materials may exhibit Negative Group Velocity (NGV). This notion directly related to Negative Group Delay (NGD), has been widely discussed and particularly in regions of anomalous dispersion by Brillouin and Sommerfeld [6]. Recently, many experimentations and theoretical studies have shown that NGV exists. For example, Wang *et al.* [7] has verified this property experimentally in optic system with gaussian pulse which presents negative group delay of 3% of the standard deviation. In fact, for a material of length L , the group delay τ and the group velocity v_g are linked by the relation $\tau = L/v_g$. This implies that for electronic circuit based on lumped elements, the length L does not have a physical significance. In this context, the group delay is a more versatile notion.

Indeed, two different teams [8]-[9] have obtained NGD with an electronic circuit built with an operational amplifier and a feedback passive circuit which is frequency limited by the component bandwidth. In the microwave domain, Eleftheriades [10] has proposed a passive left-handed resonant circuit producing a high relative NGD values in a narrow bandwidth but with high losses. To design an NGD circuit

suitable for microwave applications, a solution to compensate for such losses must be proposed. Then, in [11], we have described a new topology of active NGD microwave circuit composed of a resonant RLC series network in cascade with a FET.

This paper is devoted to the insertion of this NGD circuit in the design of a resistive balun to achieve broadband characteristics. This balun comprises an 1x2 (3dB) splitter with the association of a transmission line and an active NGD circuit in each branch. In section II, the NGD active topology is described through the design equations of a unit cell, and an experimental validation is carried out for a two-stage circuit. The following section presents the structure of the proposed balun that convert a single input to two output ports having opposite phases over a broad bandwidth. Then, the simulated performances of this NGD balun are discussed.

II. TOPOLOGY OF ACTIVE NGD CIRCUIT

The unit cell of the NGD topology (fig. 1) consists of an RLC series network in cascade with a FET [11].

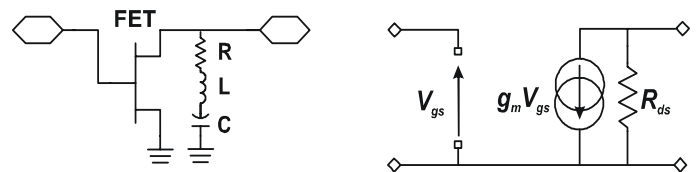


Fig. 1. Unit cell of active NGD circuit and low frequency model of the FET.

A. Synthesis Method of active NGD circuit

To get easy-to-use analytical expressions, we first model the FET by a voltage controlled current source with a transconductance, g_m , in cascade with the drain-source resistor, R_{ds} . Then, the S-parameters of this cell are defined as:

$$\begin{aligned} S_{11} &= 1, & S_{12} &= 0, \\ S_{21} &= -\frac{2g_m Z_0 R_{ds} Z}{Z_0 R_{ds} + Z(Z_0 + R_{ds})}, \end{aligned} \quad (1)$$

$$S_{22} = -\frac{Z_0 R_{ds} + Z(Z_0 - R_{ds})}{Z_0 R_{ds} + Z(Z_0 + R_{ds})}, \quad (2)$$

with $Z = R + j(L\omega - \frac{1}{C\omega})$ and $Z_0 = 50 \Omega$.

From (2) at the angular resonance frequency, $\omega_0 = 1/\sqrt{LC}$ and following the specified objectives $S_{21} = |S_{21}(\omega_0)|$ and a negative group delay $\tau = \tau(\omega_0)$, the network synthesis equations [11] are established as:

$$R = \frac{S_{21} R_{ds} Z_0}{2g_m Z_0 R_{ds} - (R_{ds} + Z_0) S_{21}}, \quad (3)$$

$$L = -\frac{\tau S_{21} g_m (R_{ds} Z_0)^2}{[2g_m R_{ds} Z_0 - (R_{ds} + Z_0) S_{21}]^2}, \quad (4)$$

and C is deduced from the resonance frequency:

$$C = \frac{1}{L\omega_0^2}. \quad (5)$$

Moreover, a resistor, R_m , is placed in parallel to match the input.

B. Experimental Validation

Using the previous synthesis relations and a final optimization including all the complete modelling, a two-stage circuit have been designed (fig. 2-a).

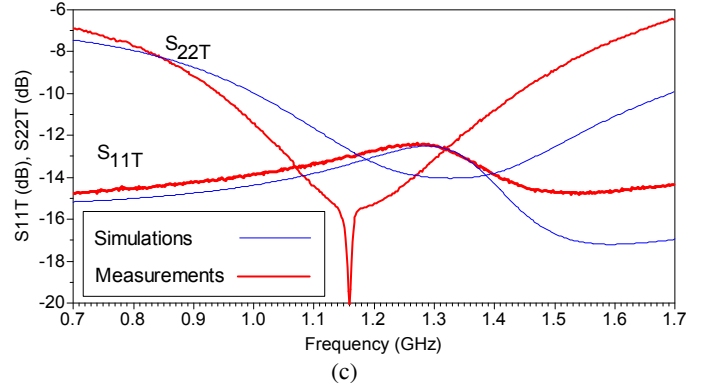
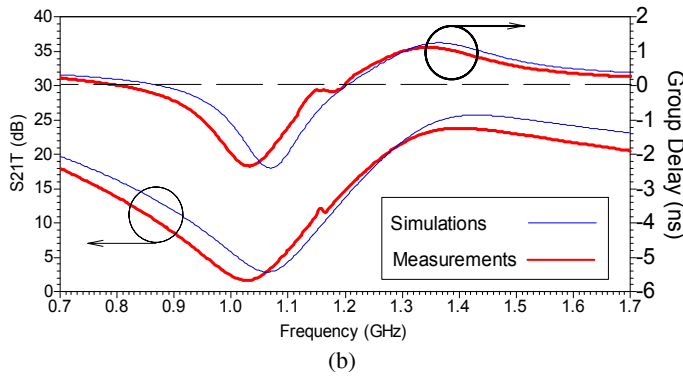
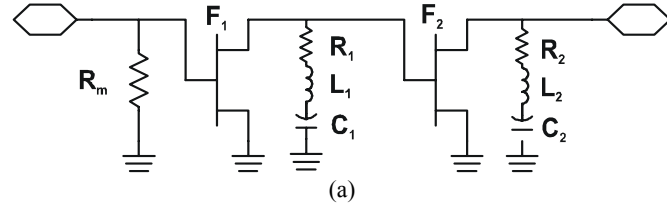


Fig. 2-a Schematic of active NGD circuit ($R_m = 75 \Omega$, $R_1 = 11 \Omega$, $R_2 = 36 \Omega$, $L_1 = L_2 = 12$ nH, $C_1 = C_2 = 1$ pF and $F_1 = F_2 = \text{PHEMT-EC2612}$ ($g_m = 98.14$ mS and $R_{ds} = 118.6 \Omega$), substrate RF35 ($\epsilon_r = 3.5$, $h = 0.508$ mm); 2-b simulated and measured transmission parameter S_{21} and group delay, 2-c comparison of simulated and measured input and output return losses.

At 1 GHz, (2) gives results really close to those obtained with the more complete model. The good agreement between simulation and measurement results (fig. 2-b and -c) both validates the Negative Group Delay topology and the synthesis equations. Indeed, at 1.07 GHz a -2.3 ns NGD is obtained with input/output matching and a gain around 2 dB. A slightly higher NGD bandwidth is achieved than from left-handed resonant approach [10] with the same number of cell. Moreover, to achieve a constant and negative group delay for a wide frequency band, many series RLC networks with different resonance frequencies [12] may be placed in parallel at the same point.

III. PROPOSED BALUN STRUCTURE

The proposed topology of balun with active NGD circuit is depicted in fig. 3 and consists of a power divider Pwd with in cascade in each branch a classical Transmission Line (TL_1 and TL_2) and a NGD circuit.

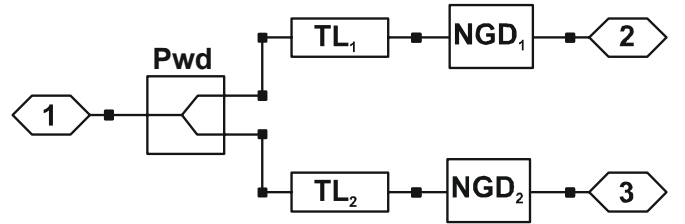


Fig. 3. Proposed architecture of balun with NGD.

The power divider ensures an equal power split associated with good return losses and isolation. In [13]-[14], Wilkinson dividers are used; then, to get opposite phases at the output of the balun, the first branch exhibits, for a specified frequency, a phase shift of $+90^\circ$ and the second one -90° , instead of -270° and -90° in a conventional balun. Here, we stay in this conventional configuration but, in each branch of the splitter, we associate a transmission line with a negative phase slope and a NGD active circuit with a positive phase slope to finally

achieve a constant phase value over a broadband defined between two frequencies f_1 and f_2 for both outputs. This principle is illustrated on fig. 4 for one branch of the proposed balun.

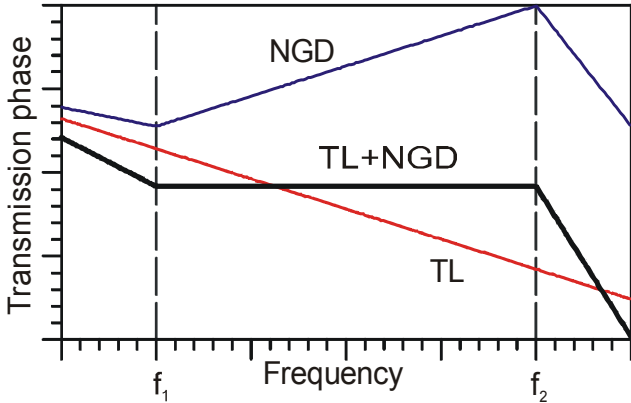


Fig. 4. Illustration of the phase behaviour in each branch.

The fig. 5 shows the schematic of the designed balun with two two-stage active NGD circuits. To keep the broadband, a 6 dB resistive splitter composed by a tree of identical resistors $Z_0/3$ is used. Then, the transmission lines TL_i ($i = \{1, 2\}$) are defined by their length, d_i , and the characteristic impedance, $Z_{c_i} = Z_0$. R_{m1} and R_{m2} allow the matching between the splitter output and the input access of the branches.

In Fig. 5-a, the top branch (TL_1 and NGD_1) shows a nearly flat phase value of $-90^\circ (\pm 10^\circ)$ and for the bottom one (TL_2 and NGD_2) a $-270^\circ (\pm 10^\circ)$ value from 3 GHz to 6 GHz.

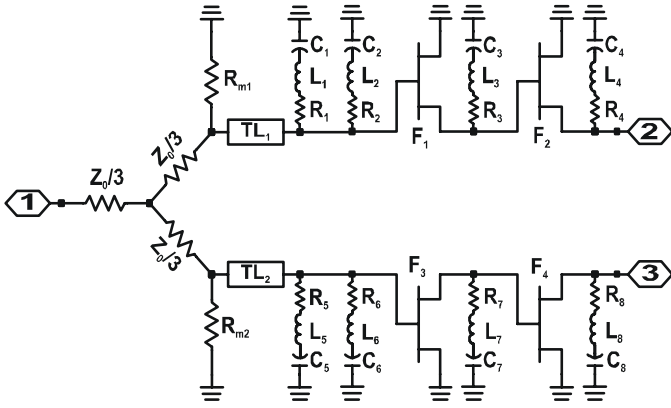


Fig. 4. Schematic of balun with active NGD circuit, $Z_0 = 50 \Omega$, $R_1 = 83 \Omega$, $R_2 = 120 \Omega$, $R_3 = 22 \Omega$, $R_4 = 42 \Omega$, $R_5 = 40 \Omega$, $R_6 = 43 \Omega$, $R_7 = 11.5 \Omega$, $R_8 = 36 \Omega$, $R_{m1} = 120 \Omega$, $R_{m2} = 150 \Omega$, $L_1 = 994 \text{ pH}$, $L_2 = 315 \text{ pH}$, $L_3 = 271 \text{ pH}$, $L_4 = 531 \text{ pH}$, $L_5 = 3.28 \text{ nH}$, $L_6 = 6.55 \text{ nH}$, $L_7 = 1.35 \text{ nH}$, $L_8 = 3.06 \text{ nH}$, $C_1 = 2.78 \text{ pF}$, $C_2 = 2.61 \text{ pF}$, $C_3 = 2.43 \text{ pF}$, $C_4 = 2.36 \text{ pF}$, $C_5 = 0.57 \text{ pF}$, $C_6 = 0.1 \text{ pF}$, $C_7 = 0.69 \text{ pF}$, $C_8 = 0.63 \text{ pF}$, TL_1 ($d_1 = 4.49 \text{ mm}$, $Z_{c1} = 50 \Omega$), and TL_2 ($d_2 = 24.89 \text{ mm}$, $Z_{c2} = 50 \Omega$) for substrate $RF35$ ($\epsilon_r = 3.5$, $h = 508 \mu\text{m}$), FETs are the same as in fig. 2.

Fig. 5-a and -b show a differential phase output of $(180^\circ \pm 9^\circ)$, insertion losses $|S_{21}|_{dB}$ and $|S_{31}|_{dB}$ of more than -2.4 dB , isolation less than -59 dB for all ports and $|S_{11}|_{dB}$

and $|S_{22}|_{dB}$ are less than -11 dB from 3 GHz to 6 GHz and the output return loss $|S_{33}|_{dB}$ is below -9 dB . Finally, $||S_{31}|_{dB} - |S_{21}|_{dB}|$ is less than 1.1 dB from 3.5 GHz to 6 GHz.

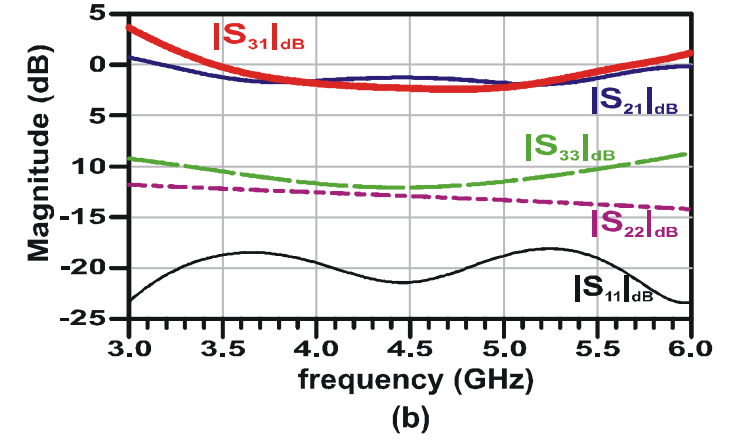
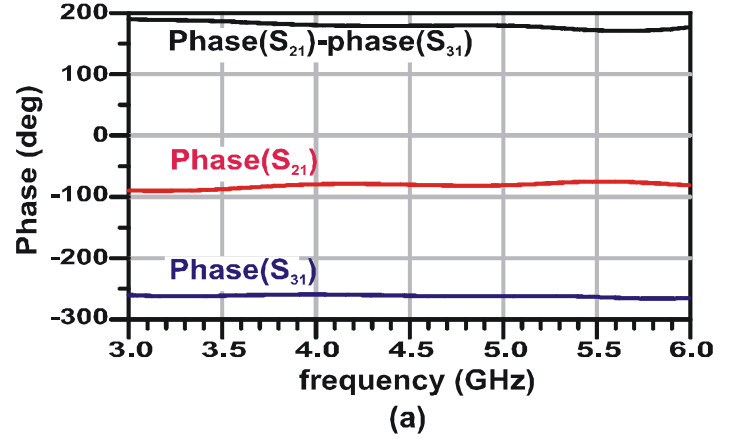


Fig.5-a. Simulated phases in degrees of S_{21} , S_{31} and their difference, and -b simulated return $S_{11}(\text{dB})$, $S_{22}(\text{dB})$ and $S_{33}(\text{dB})$, and insertion losses $S_{21}(\text{dB})$ and $S_{31}(\text{dB})$.

In table I, the variations through the two branches of the magnitude and phase of S_{21} and S_{31} are given from 3.5 GHz to 5.5 GHz.

TABLE I
PHASE AND MAGNITUDE VARIATION FROM 3.5 GHz TO 5.5 GHz

Insertion loss	Magnitude (dB)		Phase (deg.)	
	Min.	Max.	Min.	Max.
S_{21}	-1.94	-1.24	-87.47	-79.01
S_{31}	-2.40	-0.24	-263.40	-260.01

For such an active circuit, stability must be ensured for all frequencies. This circuit stability analysis has been made by carefully verifying that the magnitude of the input and output reflexion coefficients of each transistor was kept below one.

IV. CONCLUSIONS

A new balun topology based on active NGD circuit has been presented that exhibits a differential phase output of

180°(±9°) and insertion losses above -2.4dB for a frequency band of 3.5 GHz to 5.5 GHz. Thanks to the FET non-reciprocity, a good isolation of all balun ports is guaranteed. Each branch includes a phase shifter with an absolute flat phase characteristic over a broad bandwidth. This is made possible thanks to a new active topology able to deliver simultaneously gain and negative group delay or positive phase slope over a broad bandwidth. This new topology is defined by synthesis equations which have been already validated thanks to a previous realization. Active NGD circuits are particularly well-suited to be used in UWB phase shifters with constant phase versus frequency. Moreover, implementation of this topology in distributed circuit configuration is possible for higher frequency applications.

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